FIG. 1

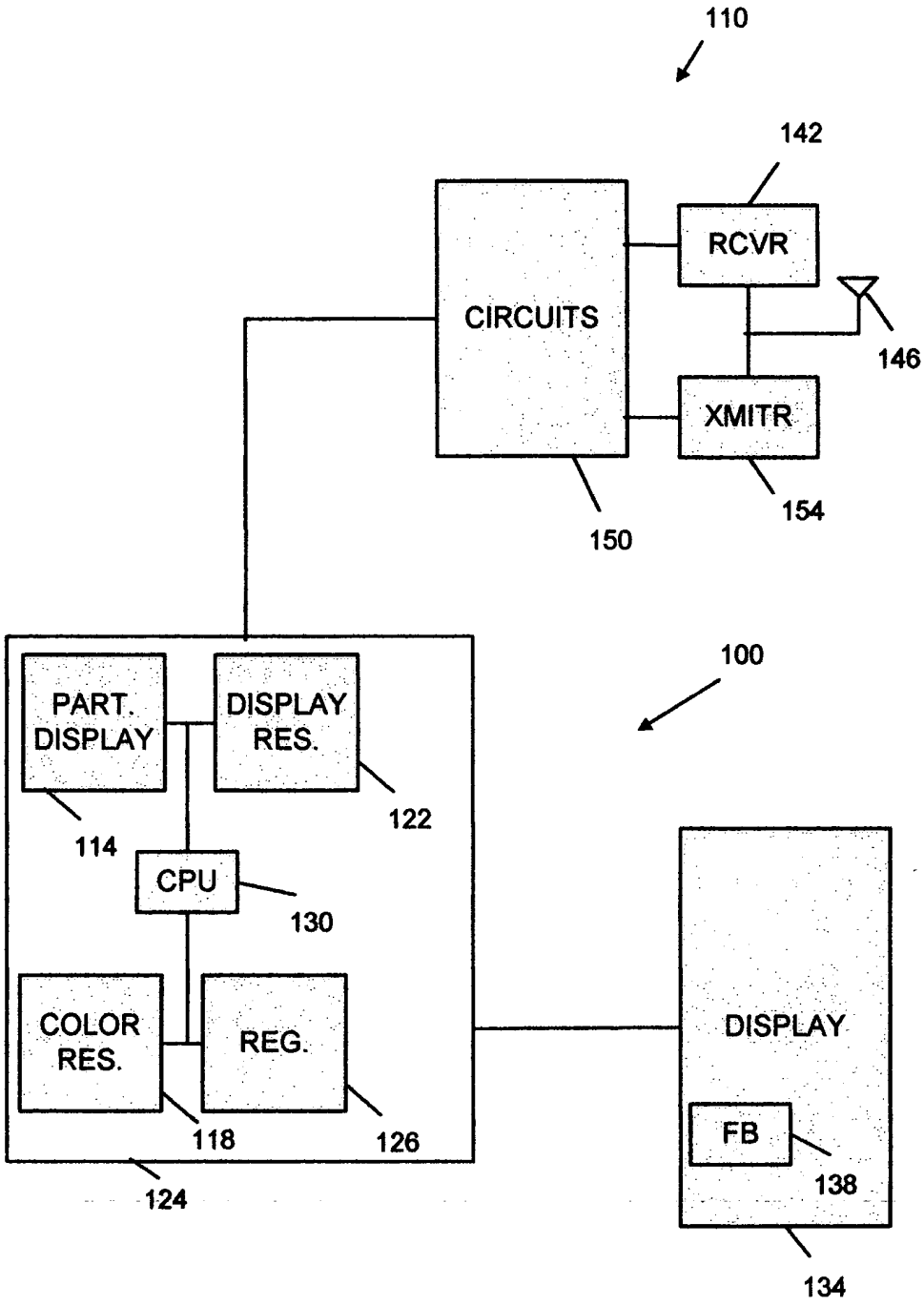
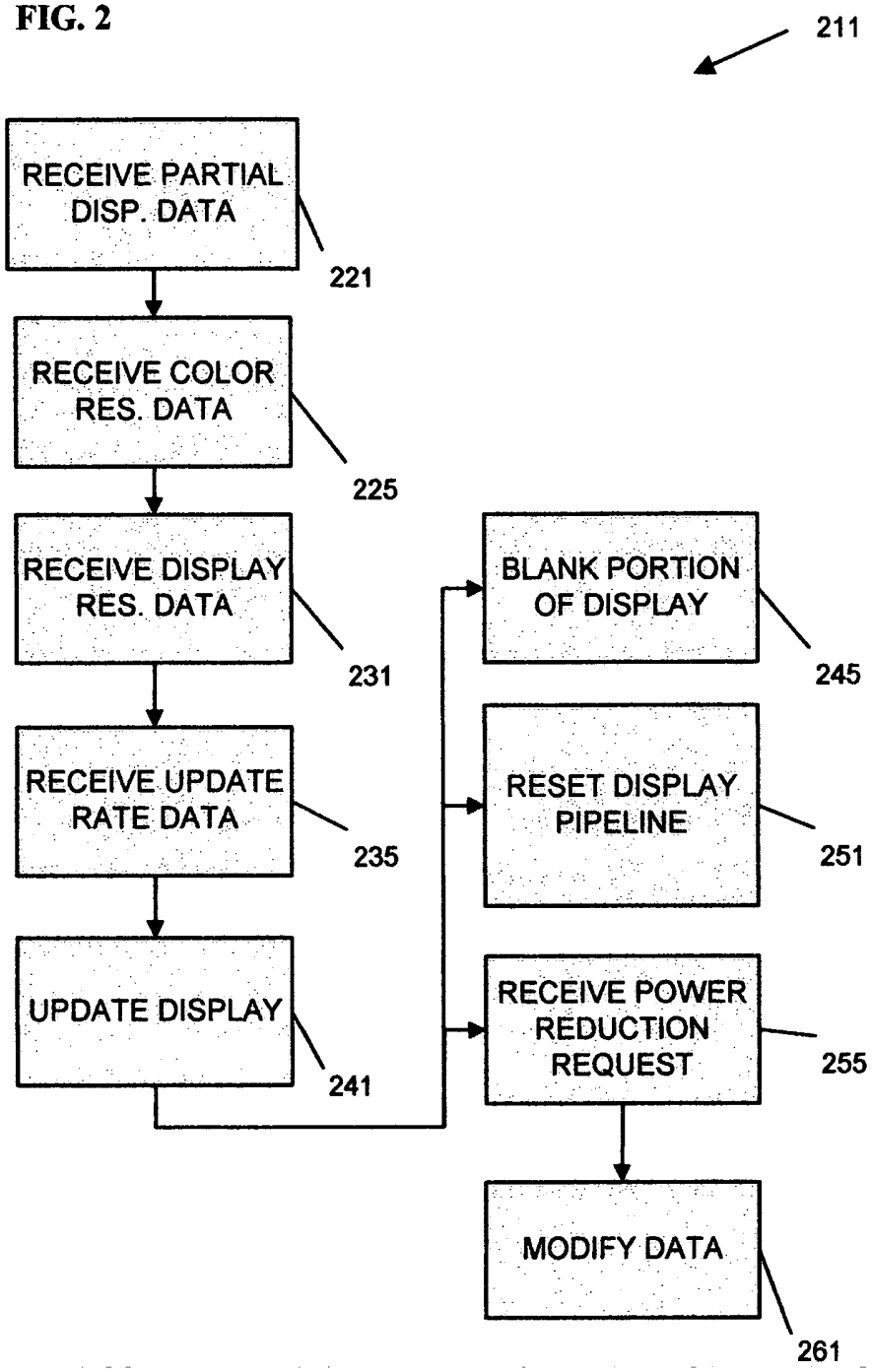

110
142
RCVR
CIRCUITS
146
XMITR
154
150
100
PART. DISPLAY
DISPLAY RES.
122
114
CPU
130
COLOR RES.
REG.
118
126
124
DISPLAY
FB
138
134

FIG. 2


211
RECEIVE PARTIAL DISP. DATA
221
RECEIVE COLOR RES. DATA
225
RECEIVE DISPLAY RES. DATA
231
RECEIVE UPDATE RATE DATA
235
UPDATE DISPLAY
241
BLANK PORTION OF DISPLAY
245
RESET DISPLAY PIPELINE
251
RECEIVE POWER REDUCTION REQUEST
255
MODIFY DATA
261

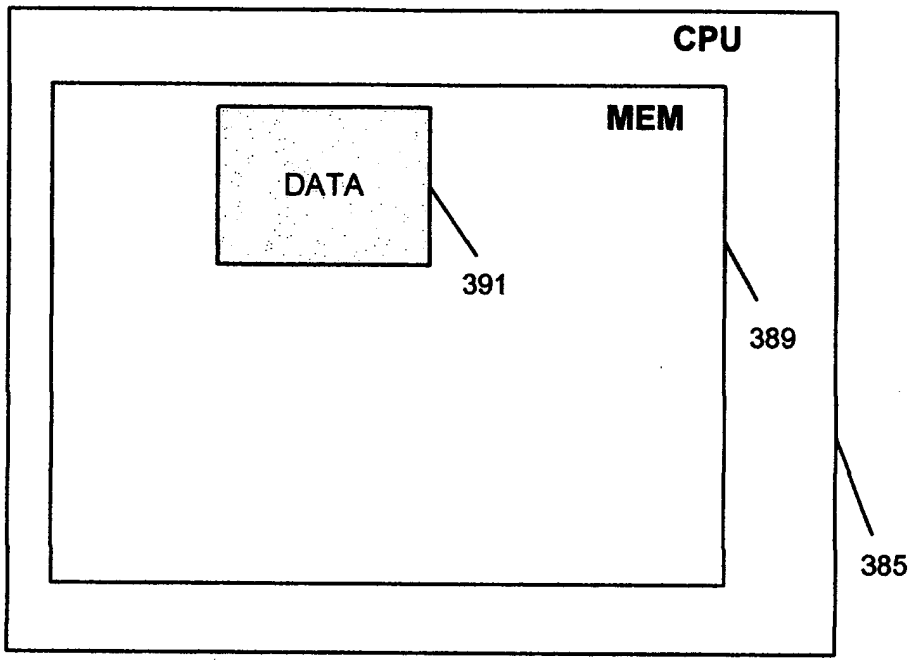



FIG. 3